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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\* SDI

This application is a DIV of 09/016,999 02/02/1998 PAT 6,596,572  
 which is a DIV of 08/623,506 03/28/1996 PAT 5,767,529

## \*\* FOREIGN APPLICATIONS \*\*\*\*\* SDI

JAPAN 7-96266 03/28/1995

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 04/28/2004

|   |          |         |        |             |
|---|----------|---------|--------|-------------|
| Foreign Priority claimed<br><input checked="" type="checkbox"/> yes <input type="checkbox"/> no   | STATE OR | SHEETS  | TOTAL  | INDEPENDENT |
| 35 USC 119 (a-d) conditions met<br><input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance | COUNTRY  | DRAWING | CLAIMS | CLAIMS      |
| Verified and Acknowledged<br>Examiner's Signature: <i>Monetta Lane</i> Initials: <i>SDI</i>   | JAPAN    | 8       | 30     | 4           |

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## TITLE

Method of manufacturing a semiconductor method of manufacturing a thin-film transistor and thin-film transistor

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|------------|---|---|
| FILING FEE | FEES: Authority has been given in Paper | <input type="checkbox"/> All Fees<br><input type="checkbox"/> 1.16 Fees ( Filing )<br><input type="checkbox"/> 1.17 Fees ( Processing Ext. of |
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